

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Date: November 19, 2007

Andreas DOERING, et al.

Confirmation No: 8071

Serial No: 10/696,865

Group Art Unit: 2183

Filed: October 30, 2003

Examiner: Dillon J. Cody

Title: METHOD AND APPARATUS FOR USING FPGA TECHNOLOGY WITH A  
MICROPROCESSOR FOR RECONFIGURABLE INSTRUCTION LEVEL  
HARDWARE ACCELERATION

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT IN REPLY TO ACTION OF JUNE 20, 2007**

In response to the Office Action dated June 20, 2007, please amend the application  
identified above as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page **2** of this  
paper.

**Remarks** begin on page **4** of this paper.